### **Amendments to the Drawings:**

The attached sheets of drawings include changes to Figs. 11M, 12, 14, 19 and 21. These sheets replace the original sheets for these figures

Attachment: Five replacement sheets

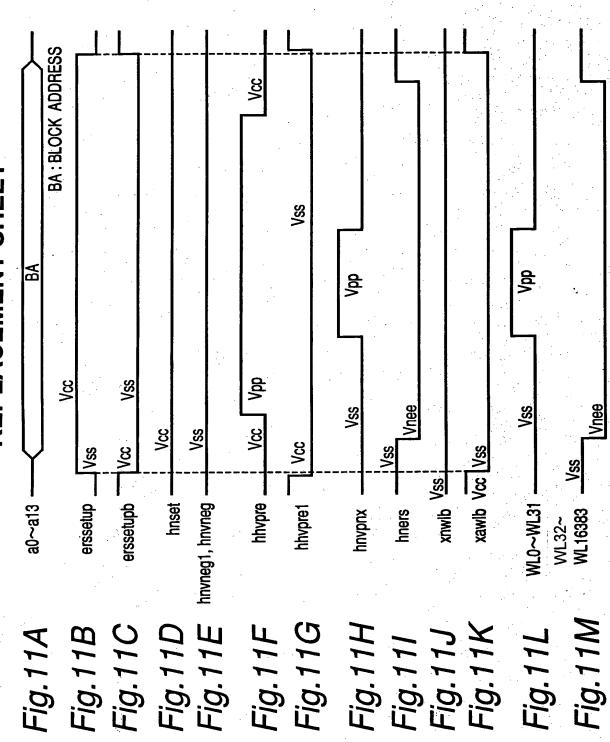
App No.: 09/598,384 Inventor: Yasuaki HIRANO

Docket No.: 204552018400

Fitle: ERASE METHOD FOR NONVOLATILE SEMICONDUCTOR STORAGE DEVICE AND ROW DECODER CIRCUIT FOR FULFILLING THE METHOD

# REPLACEMENT SHEET

SEP O 8 ZHEG



App No.: 09/598,384

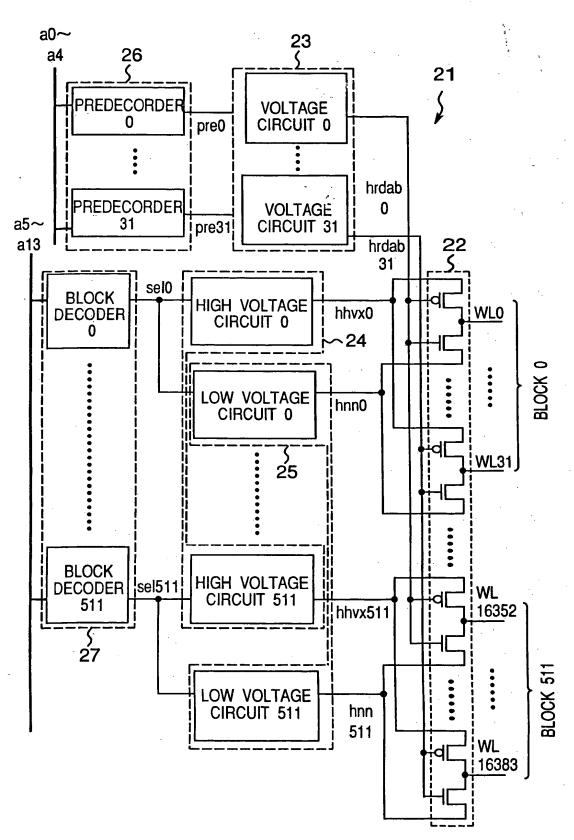
Docket No.: 204552018400

Inventor: Yasuaki HIRANO

Title: ERASE METHOD FOR NONVOLATILE

SEMICONDUCTOR STORAGE DEVICE AND ROW DECODER CIRCUIT FOR FULFILLING THE METHOD

Fig. 12 REPLACEMENT SHEET



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App No.: 09/598,384

Inventor: Yasuaki HIRANO Title: ERASE METHOD FOR NONVOLATILE

SEMICONDUCTOR STORAGE DEVICE AND ROW
DECODER CIRCUIT FOR FULFILLING THE METHOD

Docket No.: 204552018400

## REPLACEMENT SHEET

Fig.13

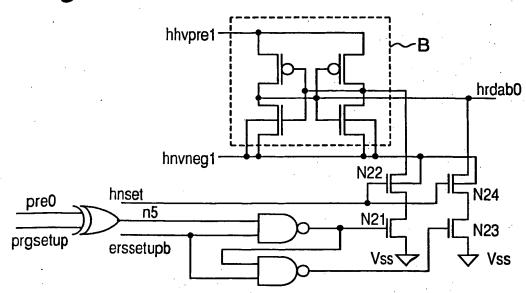
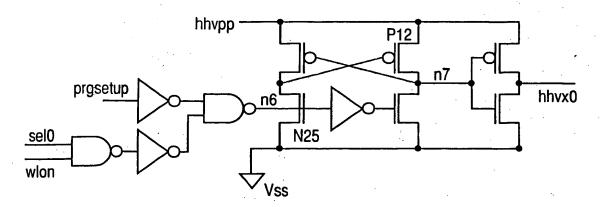


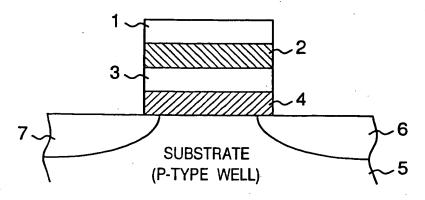
Fig.14



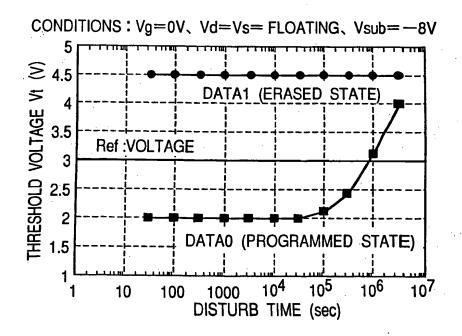
Inventor: Yasuaki HIRANO
Title: ERASE METHOD FOR NONVOLATILE
SEMICONDUCTOR STORAGE DEVICE AND ROW
DECODER CIRCUIT FOR FULFILLING THE METHOD



# REPLACEMENT SHEET Fig.19 PRIOR ART



# Fig.20 PRIOR ART



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App No.: 09/598,384

Inventor: Yasuaki HIRANO

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Docket No.: 204552018400

### **REPLACEMENT SHEET**

Fig.21 PRIOR ART

